



# Electrothermal Analysis of VLSI Systems

By Yi-Kan Cheng, Ching-Han Tsai, Chin-Chi Teng, Sung-Mo (Steve) Kang

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- Sales Rank: #8094930 in Books
- Brand: Brand: Springer
- Published on: 2000-06-30
- Original language: English
- Number of items: 1
- Dimensions: 9.21" h x .56" w x 6.14" l, 1.16 pounds
- Binding: Hardcover
- 210 pages

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## **Editorial Review**

Review

*From the Foreword:*

Continuing increases in the levels of circuit integration and concomitant increases in performance are sustaining the trend of increasing power dissipation in VLSI systems. A consequence is that the impact of temperature on the successful operation and reliability of devices must be comprehended during the design process.....This text provides a comprehensive formulation of the electrothermal analysis problem beginning with a summary of the sources of power dissipation in CMOS circuits and followed by a formulation of the effect of temperature on MOS devices.'

**Dr. Ralph K. Cavin, Vice President, Semiconductor Research Corporation**

## **Users Review**

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